

## Activity 2

### Adjoin

Input	Output
A	$Y=\bar{A}$
0	1
1	0

AND Gate

INPUT		OUTPUT
A	B	A+B
0	0	0
0	1	1
1	0	1
1	1	1

NOT Gate

Inputs		Output
A	B	$Y=A.B$
0	0	0
0	1	0
1	0	0
1	1	1

OR Gate