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**ELEC 808**  
**Class work 4 - Sum of product**

**Q1: From the truth table below, answer the following questions:**



- a) Write an SOP expression for this truth table.
- b) Draw a gate circuit diagram corresponding to that SOP expression found in question a).

**Note: A, B, C are the inputs of the circuit, Y is the output.**

Truth table:

Boolean expression 4.5 marks - each 1.5 marks  
Drawing 5.5 marks

A	B	C	Y
0	0	0	0
0	0	1	0
0	1	0	1
0	1	1	1
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	1

