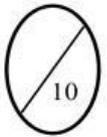


Name:		ID		Class:		Date:	
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ELEC 808
Class work 3- Converting Boolean expression to logic circuit and truth table

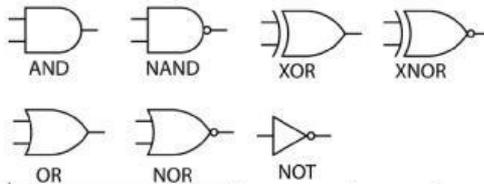


Q: Convert the following Boolean expressions to logic circuit diagrams & extract the truth table.

Construct the circuit on a breadboard.



a) $Y = (A + B)C$



1 Mark/column

3 Marks

Drawing area:

C	B	A		
0	0	0		
0	0	1		
0	1	0		
0	1	1		
1	0	0		
1	0	1		
1	1	0		
1	1	1		

Checklist

Sl.No	Task	Max marks	Mark obtained
1	Assembly on bread board correctly.	2.5	
2	Operation matching truth table.	2.5	
3	Followed safety rules.	Yes / No	
4	Total mark	5	