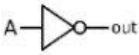


ICT – INTRODUCTION TO LOGIC GATES

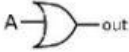
Instruction: Click and drag the correct symbol for each logic gate.

No.	Logic Gates	Symbol
1.	AND gate	
2.	NOT gate	
3.	OR gate	

Choices of symbols:



A $\rightarrow$ out



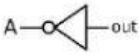
A $\rightarrow$ out



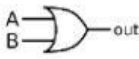
A $\rightarrow$ out
B $\rightarrow$ out



A $\rightarrow$ out
B $\rightarrow$ out



A $\rightarrow$ out



A $\rightarrow$ out
B $\rightarrow$ out

Instruction: Complete this Truth Table.

1. AND Gate

A	B	OUTPUT
1	0	0
	1	0
0	0	
1	1	

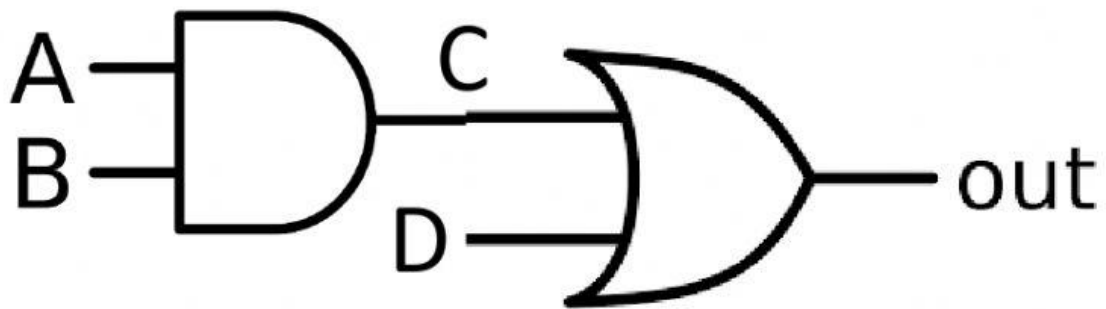
2. OR Gate

A	B	OUTPUT
0	0	0
1	1	
0		1
1	0	1

3. NOT Gate

INPUT	OUTPUT
1	
0	

Instruction: Based on this circuit diagram, complete the Truth Table.



A	B	C	D	OUTPUT
0	0		1	
0		0	0	
		1	1	
1		0		